


ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	LOGIC CIRCUITS HAVING LINEAR AND CELLULAR GATE TRANSISTORS																																																																																														
Submission Type :	Utility Patent Filing																																																																																														
Application Number:	10/709784																																																																																														
EFS ID:	61671																																																																																														
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First Named Applicant:	Victor Chan																																																																																														
Attorney Docket Number:	FIS920040113US1																																																																																														
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